

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
80	BRS	L85	2	(IFT\$1 near2 spectrum or IFFT\$1 near3 spectrum or inverse adj2 fourier near3 spectrum or IFT near3 band\$6 or IFFT\$1 near3; band\$6 or inverse adj2 fourier near3 band\$6) with (period\$3 near2 clock\$1 or sync\$1 near3 clock\$1 or synchroniz\$10 near2 clock\$1)	US- PGPUB USPAT EPO; JPO	2005/12/3 1 16:13	
81	BRS	L86	3	(IFT\$1 near2 spectrum or IFFT\$1 near3 spectrum or inverse adj2 fourier near3 spectrum or IFT near3 band\$6 or IFFT\$1 near3; band\$6 or inverse adj2 fourier near3 band\$6) same(period\$3 near2 clock\$1 or sync\$1 near3 clock\$1 or synchroniz\$10 near2 clock\$1)	US- PGPUB USPAT EPO; JPO	2005/12/3 1 16:11	
82	BRS	L87	0	genesis-alberto.in.	US- PGPUB ; USPAT ; EPO; JPO	2005/12/3 1 16:12	
83	BRS	L88	21	ginesi-alberto.in.	US- PGPUB ; USPAT ; EPO; JPO	2005/12/3 1 16:12	

	Error Definition	Err ors
80		
81		
82		
83		

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
84	BRS	L89	0	"deczky-andrew.in"	US- PGPUB ; USPAT ; EPO; JPO	2005/12/3 1 16:12	
85	BRS	L90	11	deczky-andrew.in.	US- PGPUB ; USPAT ; EPO; JPO	2005/12/3 1 16:12	
86	BRS	L91	27	88 or 90	US- PGPUB ; USPAT ; EPO; JPO	2005/12/3 1 16:12	
87	BRS	L92	3	(IFT\$1 near2 spectrum or IFFT\$1 near3 spectrum or inverse adj2 fourier near3 spectrum or IFT near3 band\$6 or IFFT\$1 near3 band\$6 or inverse adj2 fourier near3 band\$6) and 91	US- PGPUB ; USPAT ; EPO; JPO	2005/12/3 1 16:13	

	Error Definition	Err ors
84		
85		
86		
87		

				SYMBOL INTERFERENCE LIMITED CHANNELS	
<u>10209104</u>	6981186	150	07/30/2002	LOOP DIAGNOSTIC MODE FOR ADSL MODEMS	GINESI, ALBERTO
<u>10386391</u>	Not Issued	30	03/10/2003	Equalization scheme for DSL receivers in presence of an under- sampled or over-sampled transmit IDFT	GINESI, ALBERTO
<u>10930864</u>	Not Issued	30	09/01/2004	Process for providing a pilot aided phase synchronization of carrier	GINESI, ALBERTO
<u>10931236</u>	Not Issued	20	09/01/2004	Process for providing a pilot aided phase recovery of a carrier	GINESI, ALBERTO
<u>60179352</u>	Not Issued	159	01/31/2000	Shortened initialisation approach for fast retrain	GINESI, ALBERTO
<u>60179510</u>	Not Issued	159	02/01/2000	Shortened full initialisation approach for fast retrain	GINESI, ALBERTO
<u>60183679</u>	Not Issued	159	02/18/2000	Shortened full initialization approach for fast retrain	GINESI, ALBERTO
<u>60271605</u>	Not Issued	159	02/26/2001	Method and design for accurately predicting digital signal peaks for high efficiency line drivers	GINESI, ALBERTO

Inventor Search Completed: No Records to Display.

Search Another: Inventor	Last Name	First Name	<input type="button" value="Search"/>
	<input type="text" value="GINESI"/>	<input type="text" value="ALBERTO"/>	

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Day : Saturday
Date: 12/31/2005


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Time: 16:14:07

Inventor Name Search Result

Your Search was:

Last Name = DECZKY

First Name = ANDREW

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<u>09613447</u>	Not Issued	160	07/11/2000	Method and apparatus for combining voice line card and xDSL line card functions	DECZKY, ANDREW
<u>09614927</u>	6295343	150	07/12/2000	Method and apparatus for combining voice line card and xDSL line card functions	DECZKY, ANDREW
<u>09652388</u>	Not Issued	94	08/31/2000	ENHANCED LINE CARD AND PACKETIZING CPE FOR LIFELINE PACKET VOICE TELEPHONE	DECZKY, ANDREW
<u>09810938</u>	Not Issued	121	03/16/2001	System and method for programmable spectrum management	DECZKY, ANDREW
<u>09912068</u>	Not Issued	71	07/23/2001	Method and system for turbo encoding in ADSL	DECZKY, ANDREW
<u>10072615</u>	Not Issued	71	02/05/2002	System and method for data transmission in DMT-based DSL modems	DECZKY, ANDREW
<u>10274820</u>	Not Issued	30	10/18/2002	Method and system for DSL power saving	DECZKY, ANDREW
<u>10386391</u>	Not Issued	30	03/10/2003	Equalization scheme for DSL receivers in presence of an under-sampled or over-sampled transmit IDFT	DECZKY, ANDREW
<u>11036899</u>	Not Issued	30	01/15/2005	Methods for aggregating cells transported in a bonded group	DECZKY, ANDREW
<u>60617047</u>	Not Issued	159	10/12/2004	Determination of FEC parameters in DMT based xDSL systems in the presence of system imposed constraints	DECZKY, ANDREW
<u>60631775</u>	Not Issued	159	11/30/2004	Method and apparatus for the efficient implementation of a totally general convolutional	DECZKY, ANDREW

				interleaver in DMT based xDSL systems	
<u>06615730</u>	<u>4638441</u>	250	05/31/1984	DATA ACQUISITION SYSTEM FOR THE COMPUTER CONTROL OF ALUMINUM SMELTERS	DECZKY, ANDREW E.
<u>05764167</u>	<u>4294682</u>	150	01/31/1977	DATA ACQUISITION SYSTEMS	DECZKY, ANDREW EUGENE
<u>11249088</u>	Not Issued	20	10/12/2005	Methods for the determination of FEC parameters in DMT based xDSL systems in the presence of system imposed constraints	DECZKY, ANDREW G.
<u>06902988</u>	<u>4761758</u>	150	09/02/1986	DIGITAL SIGNAL PROCESSOR WITH DIVIDE FUNCTION	DECZKY, ANDREW G.
<u>07261133</u>	<u>4964118</u>	150	10/24/1988	APPARATUS AND METHOD FOR ECHO CANCELLATION	DECZKY, ANDREW G.
<u>08655400</u>	<u>5757683</u>	150	05/30/1996	DIGITAL RECEIVE FILTER FOR COMMUNICATIONS SYSTEMS	DECZKY, ANDREW G.

Inventor Search Completed: No Records to Display.

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	<input type="text" value="DECZKY"/>	<input type="text" value="ANDREW"/>	

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Inventor Name Search Result

Your Search was:

Last Name = GINESI

First Name = ALBERTO

Application#	Patent#	Status	Date Filed	Title	Inventor Name
09218054	6456654	150	12/22/1998	FRAME ALIGNMENT AND TIME DOMAIN EQUALIZATION FOR COMMUNICATIONS SYSTEMS USING MULTICARRIER MODULATION	GINESI, ALBERTO
09223817	6404806	150	12/31/1998	METHOD AND APPARATUS FOR TIME-DOMAIN EQUALIZATION IN FDM-BASED DISCRETE MULTITONE MODEMS	GINESI, ALBERTO
09223818	6693957	150	12/31/1998	ADAPTIVE FRONT END FOR DISCRETE MULTITONE MODEM	GINESI, ALBERTO
09774603	Not Issued	161	02/01/2001	Shortened full initialization of modems in a splitterless communications system	GINESI, ALBERTO
09912068	Not Issued	71	07/23/2001	Method and system for turbo encoding in ADSL	GINESI, ALBERTO
09915191	Not Issued	61	07/24/2001	Scheme for the initialization of ADSL modems	GINESI, ALBERTO
09919574	Not Issued	71	07/30/2001	Apparatus and method of loop and rate dependent power cutback	GINESI, ALBERTO
10046639	Not Issued	41	10/26/2001	Optimal bit allocation system for reed-solomon coded data	GINESI, ALBERTO
10072615	Not Issued	71	02/05/2002	System and method for data transmission in DMT-based DSL modems	GINESI, ALBERTO
10083289	Not Issued	41	02/26/2002	Method and design for accurately predicting digital signal peaks for high efficiency line drivers	GINESI, ALBERTO
10205577	Not Issued	94	07/24/2002	METHOD FOR POWER REDUCTION IN INTER-	GINESI, ALBERTO